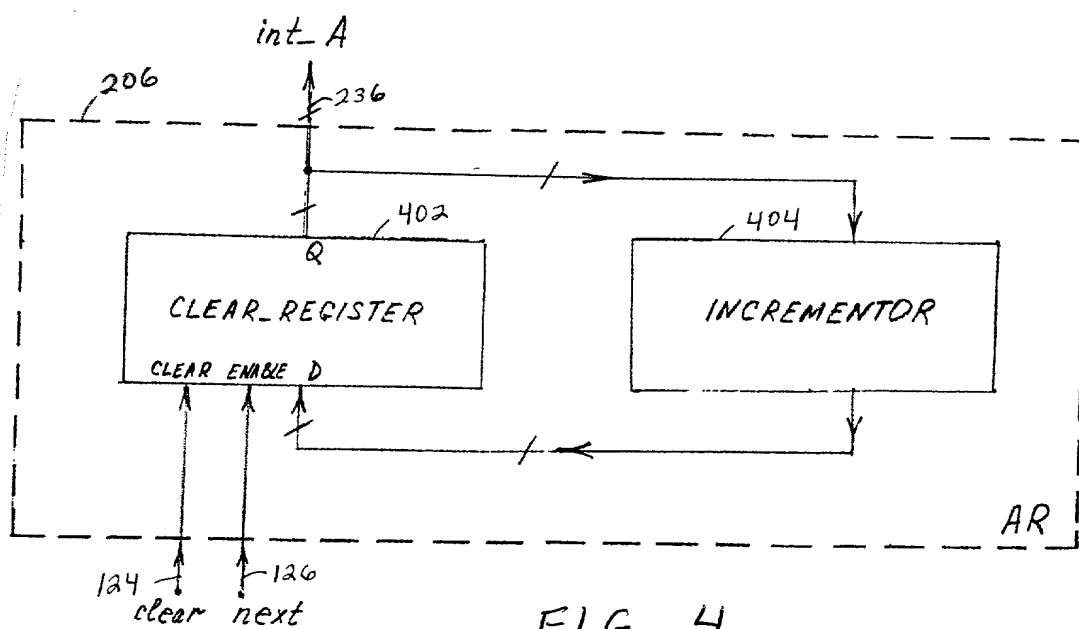
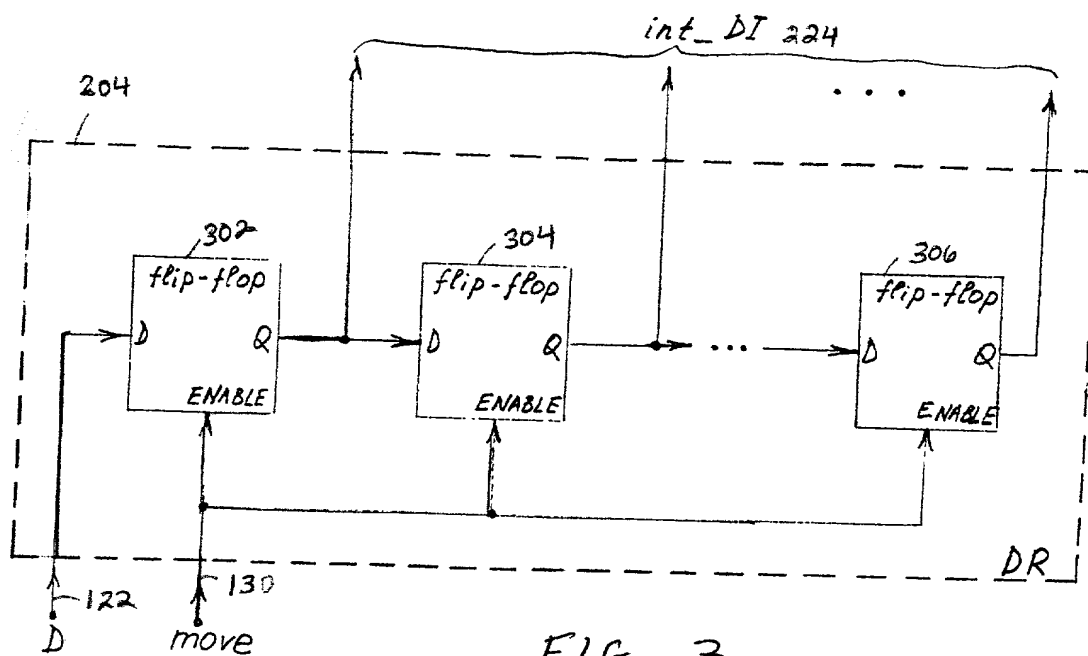


10027344 422101



500

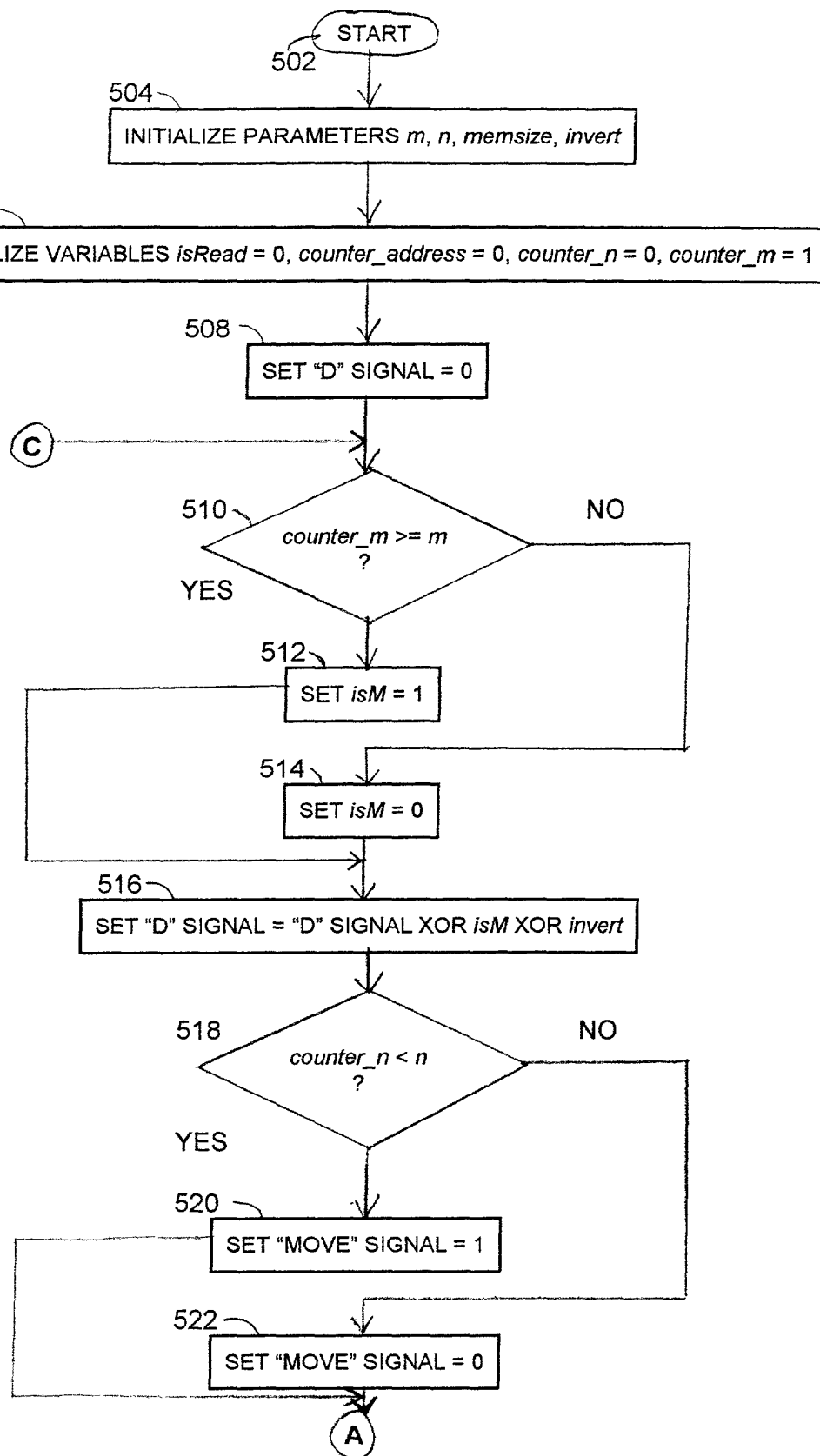


FIG. 5A

10027311 100101

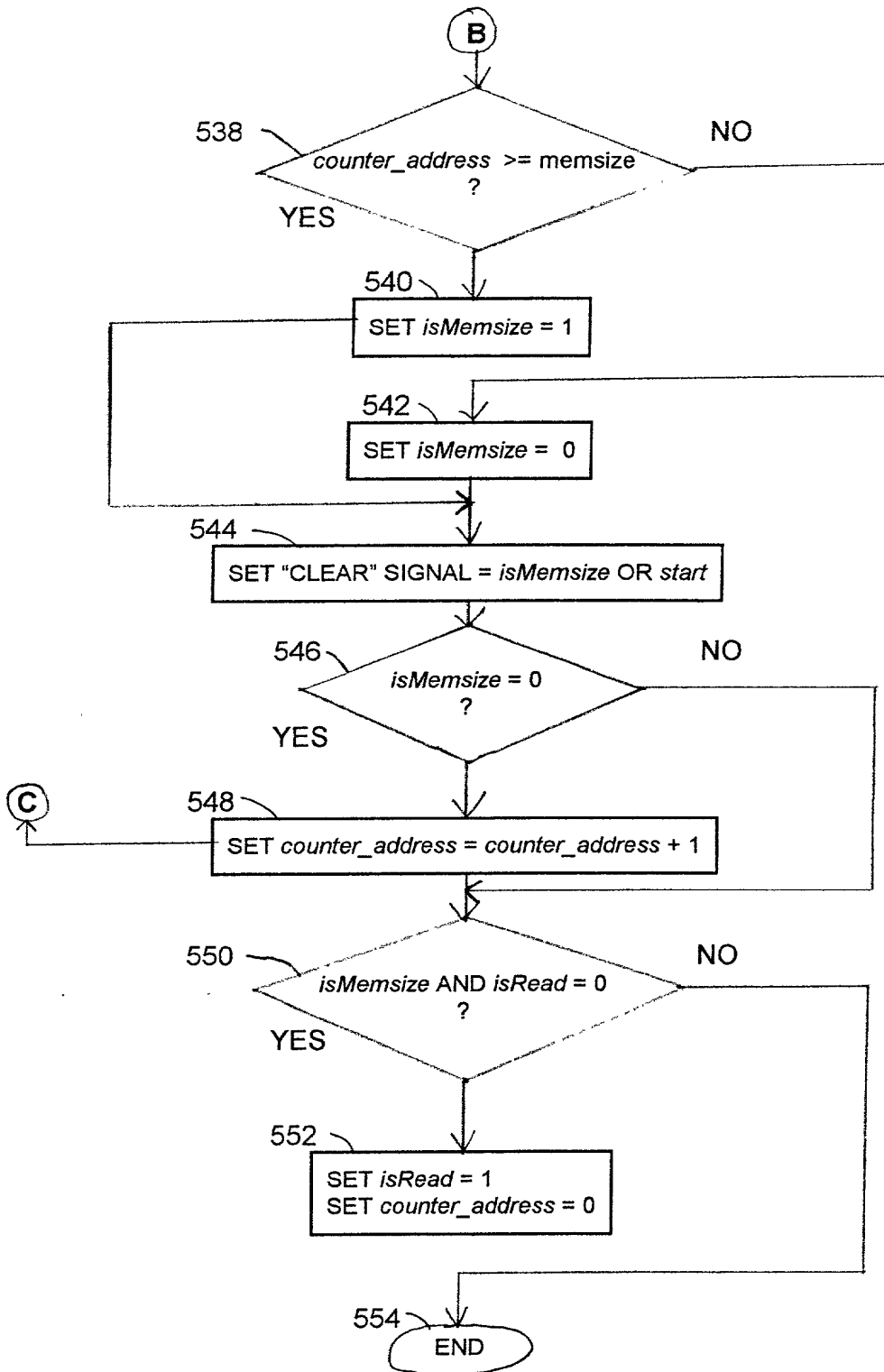


FIG. 5C

1002711-122101

	602		604		606	
	fill DR (n steps)		memory write (memsize steps)		memory read (memsize steps)	
clear	1 0 0 ...	0	0 0 ...	0	1 0 ...	0
move	1 1 1 ...	1	0 0 ...	0	0 0 ...	0
next	0 0 0 ...	0	1 1 ...	1	1 1 ...	1
we	0 0 0 ...	0	1 1 ...	1	0 0 ...	0
D	$\underbrace{0 \dots 0}_m \underbrace{1 \dots 1}_m \underbrace{0 \dots 0}_m \alpha$	α	α	α	α	α

FIG. 6

	802		804		806		808	
	fill DR (n steps)		memory write (memsize + $\lfloor \frac{\text{memsize}}{K} \rfloor$ steps)		fill DR (n steps)		memory read (memsize + $\lfloor \frac{\text{memsize}}{K} \rfloor$ steps)	
clear	1 0 0 0 ...	0	0 ...	0	1 0 0 ...	0	0 ...	0
move	1 1 1 1 ...	1	0 ... 0 1 0 ... 0 1 ...	1	1 ... 1 0 ... 0 1 0 ... 0 1 ...	1	0 ... 0 1 0 ... 0 1 ...	1
next	0 0 0 0 ...	0	1 ... 1 0 1 ... 1 0 ...	0	0 ... 0 1 ... 1 0 1 ... 1 0 ...	0	1 ... 1 0 1 ... 1 0 ...	1
we	0 0 0 0 ...	0	1 ... 1 0 1 ... 1 0 ...	0	0 ... 0 0 ...	0	0 ...	0
D	$\underbrace{0 1 0 1 \dots}_n \underbrace{\bar{\alpha} \alpha \dots}_k \underbrace{\bar{\alpha} \bar{\alpha} \dots \bar{\alpha} \alpha \dots}_k$	α	α	α	α	α	α	α

FIG. 8

700

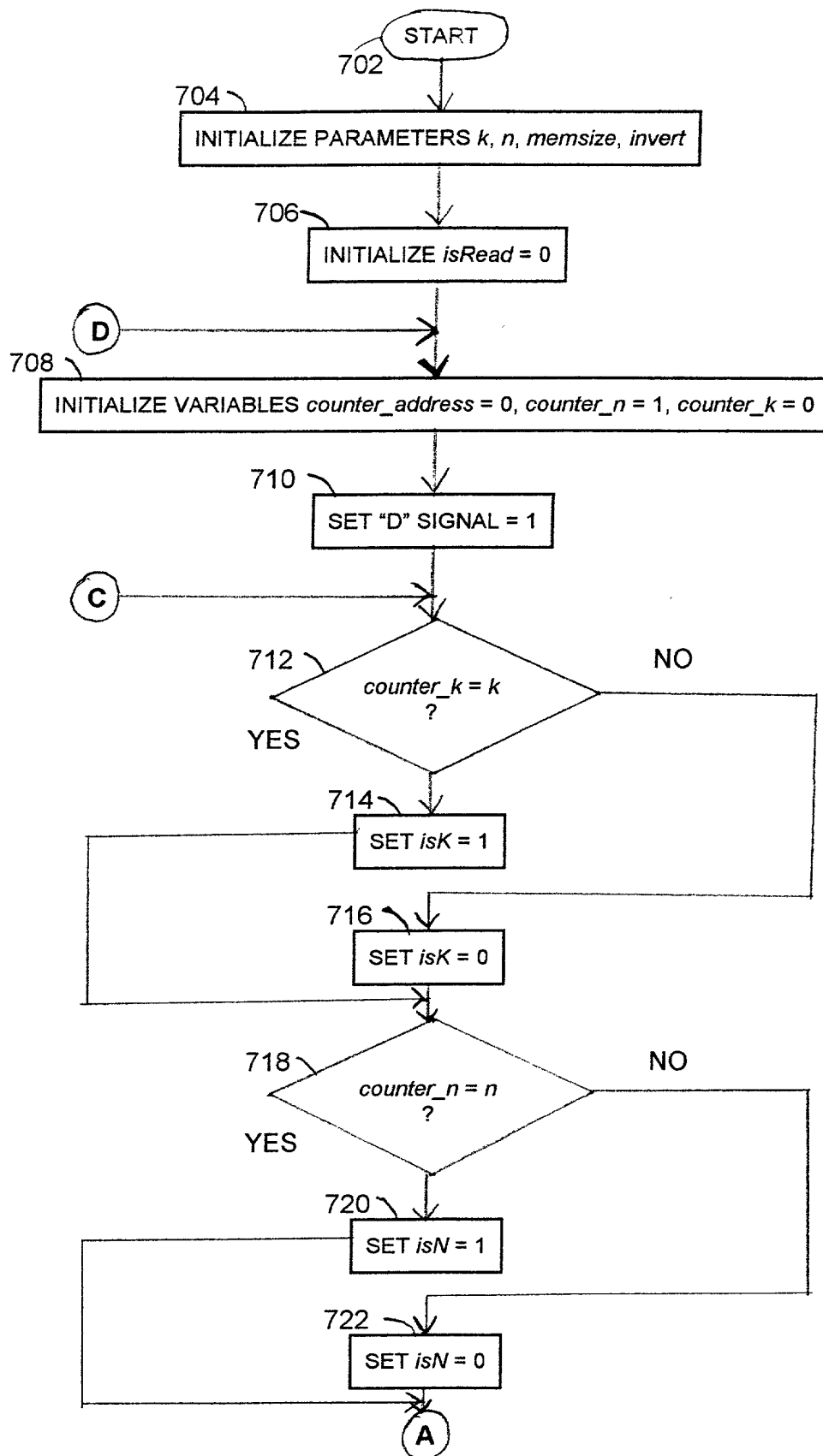


FIG. 7A

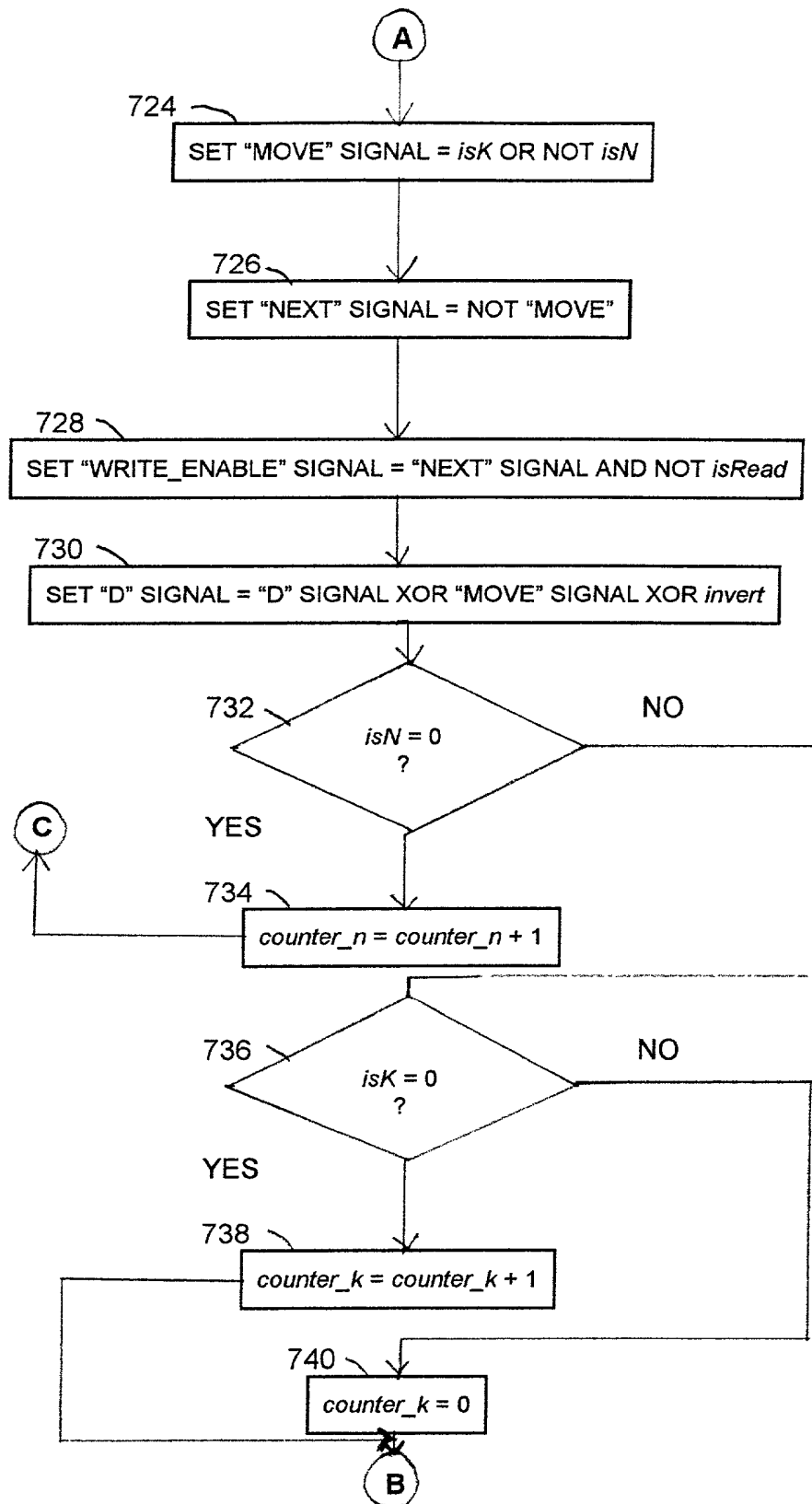


FIG. 7B

10027311.122101

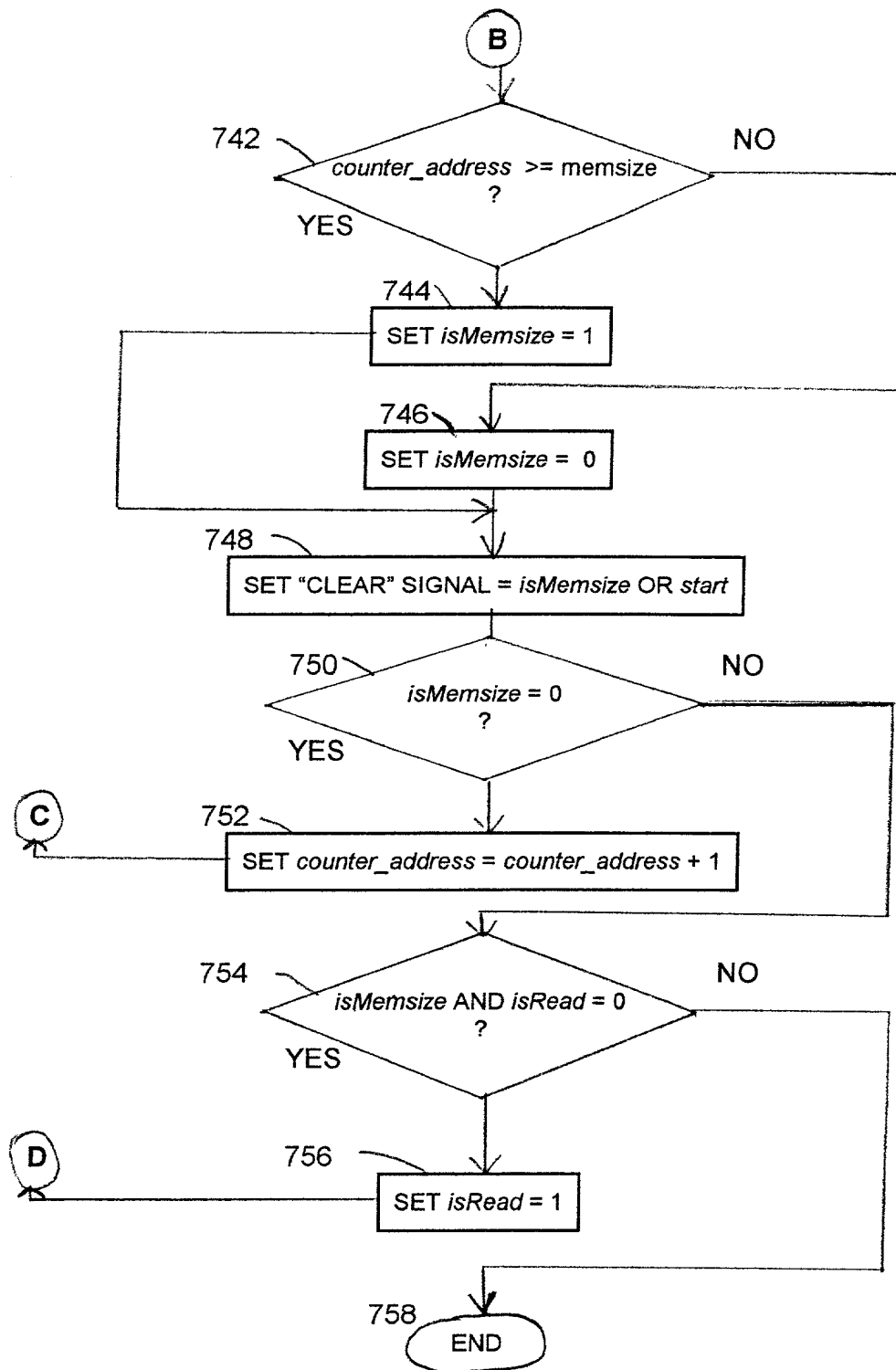


FIG. 7C